

LISTING OF CLAIMS AFTER *RESPONSE A*

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims

1 1. (1ST AMENDED per A) A computer-implemented method for dynamic emulation of legacy
2 instructions comprising:

3 accessing said legacy instructions in legacy blocks,

4 for each particular legacy instruction in a particular legacy block,

5 translating the particular legacy instruction into one or more particular

6 translated instructions for emulating the particular legacy instruction,

7 organizing the particular translated instructions into one or more particular

8 translated blocks,

9 linking the particular translated blocks into a particular linked group

10 corresponding to said particular legacy [[instruction]] block.

1 2. (ORIGINAL) The method of Claim 1 wherein said linking step uses a link in each particular
2 translated block to point to a location of the next particular translated block of the particular linked
3 group.

1 3. (ORIGINAL) The method of Claim 1 wherein said particular translated instructions are stored
2 in a cache and wherein said particular translated instructions are purged from said cache only when
3 all said particular translated instructions of particular translated blocks are also purged from said
4 cache.

1 4. (ORIGINAL) The method of Claim 1 wherein said legacy instructions are for a legacy system
2 having a S/390 architecture.

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1 5. (ORIGINAL) The method of Claim 1 wherein said legacy instructions are object code
2 instructions compiled/assembled for a legacy architecture.

1 6. (ORIGINAL) The method of Claim 1 wherein said translated instructions are for execution in
2 a RISC architecture.

1 7. (1ST AMENDED per A) A computer-implemented method for dynamic emulation of legacy
2 instructions, where the legacy instructions are compiled/assembled into object code form for a native
3 architecture, where the legacy instructions are executed as guests in the host architecture, where the
4 legacy instructions are translated to translated instructions in the host architecture and the translated
5 instructions are executed in the host architecture concurrently with the translation of the legacy
6 instructions in the host architecture, comprising:

7 accessing said legacy instructions in legacy blocks of a host system operating with said host
8 architecture,

9 for each particular legacy instruction in a particular legacy block,

10 translating the particular legacy instruction into one or more particular
11 translated instructions of the host system for emulating the particular
12 legacy instruction as a guest in said host architecture,

13 organizing the particular translated instructions into one or more particular
14 translated blocks,

15 linking the particular translated blocks into a particular linked group
16 corresponding to said particular legacy [[instruction]] block.